

### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:

Terletzki, et al.

Docket No.:

00P7882US

Filed:

September 13, 2000

Examiner:

Nguyen, Minh T.

Serial No.:

09/659,872

Art Unit:

2816

Patent No.:

6,853,233 B1

Issue Date:

February 8, 2005

For:

Level-Shifting Circuitry Having "High" Output Impedance During Disable Mode

Certificate of Corrections Branch Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

# Request for Issuance of Certificate of Correction Pursuant to 35 U.S.C. § 254 and 37 C.F.R. § 1.322

Dear Sir:

Upon review of the above-identified issued patent, Patentee notes errors that should be corrected as shown on Form PTO/SB/44, attached hereto in duplicate.

Some of the mistakes noted on the attached Certificate of Correction were not incurred through the fault of the Patent Office. Accordingly, please charge the required fee of \$100 for this Request for Certificate of Correction, and any other required fees to Deposit Account 50-1065.

Patentee respectfully solicits the issuance of the requested Certificate of Correction.

April 15, 2005

Date

Ira S. Matsil

Reg. No. 35,272

Respectfully submitted,

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# UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO. :

: 6,853,233 B1

DATED

: February 28, 2005

INVENTOR(S)

: Terletzki, et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the front page, paragraph (54), "Höhenkirchen" should be --Höhenkirchen-Siegertsbrunn--;

Column 1, line 33, " $N_1$ , and  $P_2$ " should be -- $N_1$  and  $P_2$ --;

line 62, "interchangeable" should be --interchangeably--;

line 64, "electrode)." should be --electrode.)--

Column 3, line 62, delete the word "and";

Column 4, line 1, " $N_1$ ,  $P_2$ " should be -- $N_1$  and  $P_2$ --;

line 22, "thus" should be --that--;

Column 5, line 18, "claims" should be --claim--;

line 35, "sia" should be --signal--;

Column 6, line 19, insert ";" immediately following --source/drain--;

line 20, "n-cannel" should be --n-channel--;

line 32, "fast" should be --first--;

Column 7, line 3, "source drain" should be --source/drain--;

line 5, "to gate" should be --to the gate--;

line 22, "that" should be --than--;

Column 8. line 20. "second" should be --section--:

line 41, "series the" should be --series with the--;

line 43, "series the" should be --series with the--; and

line 51, "hat" should be --that--.

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No. of additional copies 0

PATENT NO: 6,853,233 B1

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Applicant: Terletzki, et al. Docket No.: 00P7882US

Serial No.: 09/659,872 Art Unit: 2816

Filed: September 13, 2000 Examiner: Nguyen, Minh T.

Patent No.: 6,853,233 B1 Issued: February 8, 2005

For: Level-Shifting Circuitry Having "High" Output Impedance During Disable

Mode

# Certificate of Mailing via First Class Mail (37 C.F.R. § 1.8(a))

Date of Deposit: April 15, 2005

I hereby certify that the below listed correspondence is being deposited with the United States Postal Service on the date indicated above as first class mail in an envelope addressed to: Mail Stop Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Certificate of Mailing via First Class Mail (1 page) Request for Certificate of Correction (1 page) Certificate of Correction (2 duplicate pages) Return Postcard

Respectfully submitted,

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